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## Patent Abstracts of Japan

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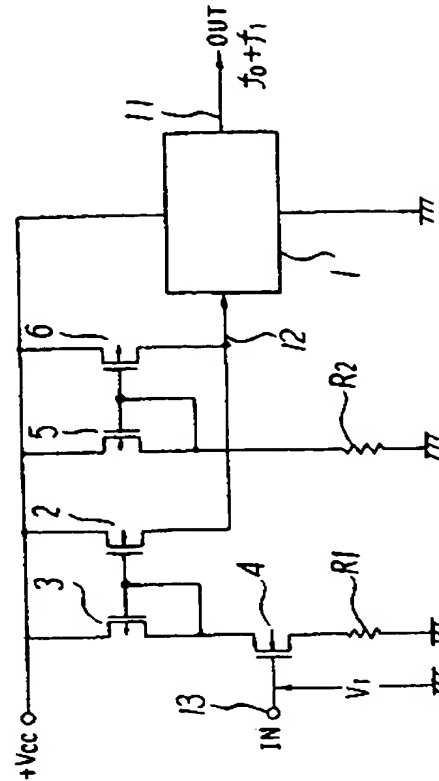
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APPLICANT : NEC CORP;

INVENTOR : NAKANISHI YOSHINOBU;

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TITLE : VOLTAGE CONTROLLED  
OSCILLATING CIRCUIT



ABSTRACT : PURPOSE: To make input voltage-oscillating frequency characteristics variable, by controlling an input current of a current control oscillating circuit with an input voltage and a constant current circuit provided additionally.

CONSTITUTION: A drain of a P FET2 is connected to an input side 12 of a current control oscillating circuit 1, and a gate of the FET2 is connected to a gate and a drain of a P FET3 and a source of an N FET4. Further, a gate of the FET4 is connected to an input terminal 13. Moreover, the input side 12 of the oscillating circuit 1 is connected to a drain of a P FET6, the gate is connected to a gate and a drain of a P FET5 and grounded via a resistor R<sub>2</sub>. Thus, the input current of the oscillating circuit 1 is changed depending on an input voltage to the terminal 13 and the resistor R<sub>2</sub>, allowing to make the input voltage-oscillating frequency characteristics variable.

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